TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

## 2SK2744

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

4-V gate drive

• Low drain-source ON resistance:  $R_{DS}$  (ON) = 15 m $\Omega$  (typ.)

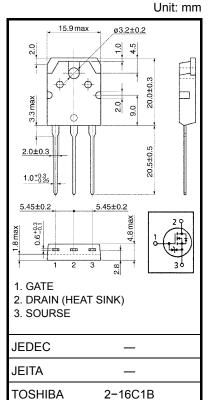
• High forward transfer admittance: |Y<sub>fS</sub>| = 27 S (typ.)

Low leakage current: I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 50 V)

• Enhancement mode:  $V_{th}$  = 1.5 to 3.5 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	50	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	50	<b>V</b>	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC (Note 1)	I <sub>D</sub>	45	А	
Diam current	Pulse (Note 1)	I <sub>DP</sub>	180		
Drain power dissipation	n (Tc = 25°C)	$P_{D}$	125	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	95	mJ	
Avalanche current		I <sub>AR</sub>	45	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	12.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.0	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 25$  V,  $T_{ch} = 25$ °C (initial), L = 58  $\mu H$ ,  $R_G = 25$   $\Omega$ ,  $I_{AR} = 45$  A

Note 3: Repetitive rating: pulse width limited by maximum junction temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

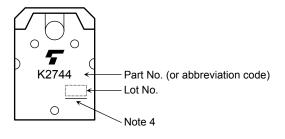
### **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-off curre	ent	I <sub>DSS</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	100	μА
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	50	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	_	3.5	V
Drain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A	_	15	20	mΩ
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 25 A	15	27	_	S
Input capacitance		C <sub>iss</sub>		_	2300	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	420	_	pF
Output capacitance		C <sub>oss</sub>		_	1200	_	pF
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 25 \text{ A} \\ V_{GS} = 25 \text{ V}$ $V_{DD} \approx 25 \text{ V}$ $V_{DD} \approx 25 \text{ V}$ $V_{DD} \approx 25 \text{ V}$	_	30	_	- ns
	Turn-on time	t <sub>on</sub>		_	45	_	
	Fall time	t <sub>f</sub>			80	_	
	Turn-off time	t <sub>off</sub>		l	230		
Total gate charge (gate-source plus gate-drain)		Qg	V <sub>DD</sub> ≈ 40 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 45 A	_	68	_	nC
Gate-source charge		Q <sub>gs</sub>	עטט = אין v, vGS – וט v, וט – 45 A	_	20	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	48	_	nC

## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	45	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	180	Α
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = 45 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.8	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 45 A, V <sub>GS</sub> = 0 V	_	130	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 50 A/μs	_	0.3	_	μС

#### Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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